

SSC8027GS8

P-Channel Small Switching MOSFET

> Features

V _{DS}	V _{GS}	R _{DS(ON)} Typ.	ID
-20V	+12V	105mΩ@-4.5V	-2.5A
-20 V	12 V	130mΩ@-2.5V	-2.5/

Description

This device is an P-Channel enhancement mode MOSFET, with low on-resistance, fast switching speed and low threshold voltage, it is ideal for portable equipment.

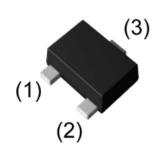
Applications

- Direct Logic-Level Interface: TTL/CMOS
- Drivers: Relays, Solenoids, Lamps, Hammers
- Display, Memories, Transistors, etc.
- Battery Operated System
- Solid-State Relays

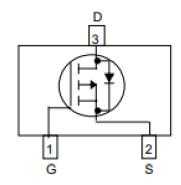
> Ordering Information

Device	Package	Shipping
SSC8027GS8	SOT-523	3000/Reel

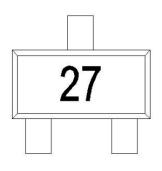
Pin configuration



SOT-523



Pin Configuration (Top View)



Marking



➤ Absolute Maximum Ratings (T_A=25°C unless otherwise noted)

Symbol	Parameter	Ratings	Unit
V _{DSS}	Drain-to-Source Voltage	-20	V
V _{GSS}	Gate-to-Source Voltage	±12	V
I _D	Continuous Drain Current ^a	-2.5	Α
I _{DM}	Pulsed Drain Current b	-10	Α
P _D	Power Dissipation ^c	0.7	W
TJ	Operation junction temperature	-55~150	$^{\circ}$ C
T _{STG}	Storage temperature range	-55~150	$^{\circ}$

➤ Thermal Resistance Ratings (T_A=25°C unless otherwise noted)

Symbol	Parameter	Maximum	Unit
ReJA	Junction-to-Ambient Thermal Resistance a	172	°C/W

Note:

- a. The value of R_{θJA} is measured with the device mounted on 1 in² FR-4 board with 2oz.copper, in a still air environment with T_A=25°C. The value in any given application depends on the user is specific board design. The power dissipation is based on the t≤10s thermal resistance rating.
- b. Repetitive rating, pulse width limited by junction temperature.
- c. The power dissipation P_D is based on T_{J(MAX)}=150°C, using junction-to-case thermal resistance, and is more useful in setting the upper dissipation limit for cases where additional heat sinking is used.

SSC-V1.0 www.sscsemi.com Analog Future



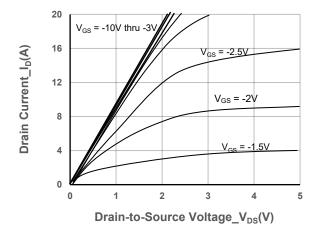


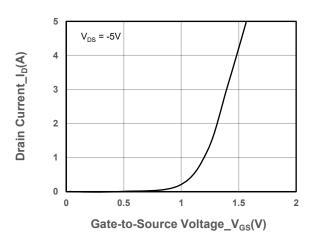
\succ Electrical Characteristics (T_A=25°C unless otherwise noted)

Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} = 0V, I _D = -250μA	-20			V
Gate Threshold Voltage	$V_{\text{GS(th)}}$	$V_{DS} = V_{GS}, I_{D} = -250uA$	-0.4	-0.7	-1	V
Drain-Source On-Resistance	_	V _{GS} = -4.5V, I _D = -0.45A		105	150	0
Diam-Source On-Resistance	R _{DS(on)}	V _{GS} = -2.5V, I _D = -0.35A		130	230	mΩ
Zero Gate Voltage Drain Current	IDSS	V _{DS} =-20V, V _{GS} = 0V			-1	μA
Gate-Source Leak Current	Igss	V _{GS} = ±12V, V _{DS} = 0V			±100	nA
Forward Voltage	V _{SD}	V _{GS} = 0V, I _S = -0.5A		-0.7	-1.3	V
Input Capacitance	Cıss	\\ - 40\\\\ - 0\\		370		pF
Output Capacitance	Coss	$V_{DS} = -10V, V_{GS} = 0V,$		161		
Reverse Transfer Capacitance	Crss	f = 1MHz 27				
Turn-on Delay Time	T _{D(ON)}			9.8		
Rise Time	Tr	$V_{GS} = -4.5V, R_L = 4\Omega$		24]
Turn-off Delay Time	T _{D(OFF)}	V_{DS} = -10V, R_G = 2Ω		18		ns
Fall Time	T _f			7.7		
Total Gate Charge	Q _G	45/// 40//		4.1		
Gate to Source Charge	Q _{GS}	$V_{GS} = -4.5V, V_{DS} = -10V,$		0.4		nC
Gate to Drain Charge	Q _{GD}	- I _D =-0.2A		0.5		

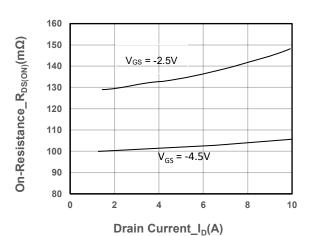


> Typical Performance Characteristics (T_A=25℃ unless otherwise noted)

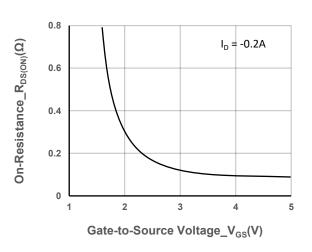




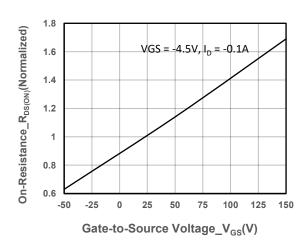
Output Characteristics



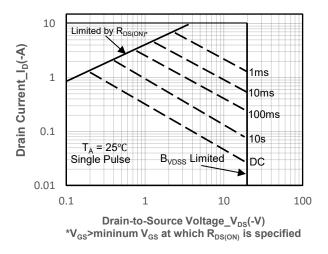
Transfer Characteristics



On-Resistance vs. Drain Current and Gate Voltag



On-Resistance vs. Gate-to-Source Voltage



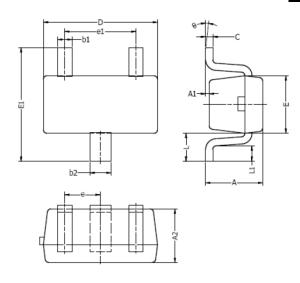
On-Resistance vs. Junction Temperature

Safe Operating Area vs. Junction-to-Ambient

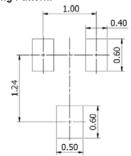


Package Information

SOT-523



Typica	l Sol	derina	Pattern:



DIM	MILLIMETERS		INCHES	
DIM	MIN	MAX	MIN	MAX
Α	0.70	0.90	0.028	0.035
A1	0.00	0.10	0.000	0.004
A2	0.70	0.80	0.028	0.031
b1	0.15	0.25	0.006	0.010
b2	0.25	0.35	0.010	0.014
С	0.10	0.20	0.004	0.008
D	1.50	1.70	0.059	0.067
Е	0.70	0.90	0.028	0.035
E1	1.45	1.75	0.057	0.069
е	0.50	0.50 TYP.		TYP.
e1	0.90	1.10	0.035	0.043
L	0.40 REF.		0.016	REF.
L1	0.10	0.30	0.004	0.012
θ	0 °	8°	0 °	8°

- NOTES:

 1. Above package outline conforms to JEITA EAIJ ED-7500A SC-75A.

 2. Dimensions are exclusive of Burrs, Mold Flash & Tie Bar extrusions.



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